•		EAD! Oction moc	. .			
Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S5	8	stubless near5 configur\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 11:58
L3	1003555	terminat\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 12:04
S10	43655	memory adj3 (module or board)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 12:04
S21	934837	terminat\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 12:04
L4	1680	2 same 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 12:05
L5	7297	bus near5 L3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 12:05
L8	12	stubless and L6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON .	2006/03/31 12:56
L46	1245	365/52	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON .	2006/03/31 14:56
L47	3535	365/63	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:01
L48	1846	361/728	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:01
L49	751	361/729	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:01
L51	4	4 and 49	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:01

L50	9	4 and 48	US-PGPUB;	OR	ON	2006/03/31 15:03
LSU	9	4 anu 48	USPAT; EPO; JPO; DERWENT; IBM_TDB	OK	ON	2000/03/31 13.03
.L52	76	4 and 46	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:03
L53	108	4 and 47	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:03
L54	.39	6 and 47	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:03
L55	. 27	6 and 46	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:03
L57	20	54 not 55	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:15
L60	3	4 same 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:15
L61	. 3	4 and 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:15
L59	0	6 and 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:20
L64	1	5 and 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:21
L65	26	2 and 56	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:21
L66	318	memory with bus with architecture with configur\$8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 15:25

L69	9	("5524232" "5578940" "6411539" "6449166" "6480409" "6522165" "6690191" "6711027" "6754132").PN. OR ("7016212").URPN.	US-PGPUB; USPAT; USOCR	OR .	ON	2006/03/31 17:37
L70	697	2 with 3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:37
L6	299	2 same 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:38
L72	144	2 with 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:39
L73	24366782	@AD < "20030728"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:39
L74	108	72 and 73	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:39
L2	71245	memory near3 (module or board)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:45
L77	19	66 and 75	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:46
L78	532	2 same 75	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:46
L79	439	2 with 75	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:46
L56	206	(loop adj through) adj5 (form or config\$8)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:47
L80	219	((loop adj through) or stubless or stabless)adj5 (form or config\$8)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:48

L81		79 and 80	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:48
L75	9269	memory near5 terminat\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:51
L82	80	(memory adj2 chip) near5 terminat\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:51
L83	51	73 and 82	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/03/31 17:51